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Substitute for form 1449A/PTO

Sheet

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

of

Application Number	10/600,319
Filing Date	June 20, 2003
First Named Inventor	Mark J. Pavicic
Art Unit	2819
Examiner Name	Not Yet Known JEANPIER PE
Attorney Docket Number	14676.01

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Substitute for form 1449A/PTO **Application Number** 10/600,319 RADEMARK **Filing Date** June 20, 2003 INFORMATION DISCLOSURE First Named Inventor Mark J. Pavicic STATEMENT BY APPLICANT **Art Unit** (use as many sheets as necessary) Not Yot Known JEANPIERRO **Examiner Name** Sheet of **Attorney Docket Number** 14676.01 OTHER DOCUMENTS - NON-PATENT LITERATURE DOCUMENTS Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the TRANSLATION *Examiner Cite item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue Initials No. number(s), publisher, city and/or country where published YES NO G.M. Haller, "High-Speed, High-Resolution Analog Waveform Sampling in VLSI Technology", Ph.D. Dissertation, Technical Report No. ICL94-030, Sanford University, March 1994; pp. 83-88. G.M. Haller & B.A. Wooley, "A 700-MHz Switched-Capacitor Analog Waveform Sampling Circuit", IEEE J Solid-State Circuits, Vol. 29, No. 4, April 1994; pp. 500-508. П D. Lachartre & F. Feinstein, "Application Specific Integrated Circuits For ANTARES Offshore Front-end Electronics", 2nd Int'l. Conf. on New Developments in Photodetection, Beaune, France, 1999, P/-5. F. Feinstein, "The ANTARES digital electronics scheme", Intl Cosmic Ray Conf, 1999. P. 1-4. F. Druillole et al., "The Analogue Ring Sampler: An ASIC for the Front-End Electronics of the ANTARES Neutrino Telescope", IEEE Nuc Sci Sym, Nov 2001, P | -/ 8 П "Conceptual Design Report of the 0.1 km² Detector", ANTARES Internal Doc, Dec. 1999; pp. 27-28, 42-43. PIP J.P. Denance, P. Nayman, F. Toussenel, & M. Punch, "Proposal for the HESS experiment electronics", LPNHE-Paris, HESS Electronics, Feb 1999; pp. 1-6. C. Bronnimann, R. Horisberger, & R. Schnyder, "The domino sampling chip: a 1.2 GHz waveform sampling CMOS chip", Nuc Inst & Meth in Phy Res, A420, 1999; pp. 264-269. П S.A. Kleinfelder, "A Multi-Gigahertz Analog Transient Recorder Integrated Circuit", MS Thesis, UC-Berkeley, 1992; pp. 8-13. S.A. Kleinfelder, "A Multi-GHz, Multi-Channel Transient Waveform Digitization Integrated Circuit", 2002, 9/5 G.T. Przybylski, "Analog Transient Waveform Digitizer R1.1 (V2)", LBNL, 2001. (http://rust.lbl.gov/~gtp/ATWD/atwd_manual3.doc). Doc page. **EXAMINER SIGNATURE DATE CONSIDERED**

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Application Number 10/600,319

Filing Date June 20, 2003

First Named Inventor Mark J. Pavicic

Art Unit 2819

Examiner Name Not Yet Known JEANTIERES

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Attorney Docket Number

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U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE Substitute for form 1449A/PTO **Application Number** 10/600,319 June 20, 2003 Filing Date INFORMATION DISCLOS Mark J. Pavicic **First Named Inventor** STATEMENT BY APPLICANT 2819 **Art Unit** (use as many sheets as necessary) **Not Yet Known Examiner Name Attorney Docket Number** 14676.01 2 of Sheet 1 **U.S. PATENT DOCUMENTS** Pages, Columns, Lines, DOCUMENT NUMBER Where Relevant Passages or Name of Patentee or **Publication Date** Relevant Figures Appear MM-DD-YYYY **Applicant of Cited Document** Number - Kind Code (if known) US-US-US-US-US-US-US-US-US-US-US-US-US-US-FOREIGN PATENT DOCUMENTS FOREIGN PATENT DOCUMENT TRANSLATION Pages, Columns, Lines, Number - Kind Code Country **Publication Date** Where Relevant Passages Name of Patentee or (if known) Code: or Relevant Figures Appear **Applicant of Cited Document** MM-DD-YYYY П DATE CONSIDERED EXAMINER SIGNATURE P. JeanPierre *EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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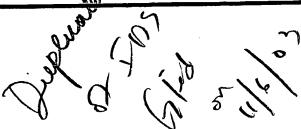
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